

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)
B.Tech II Year II Semester Supplementary Examinations May/June-2024
COMPUTER ORGANIZATION
(Common to CSE & CSIT)

Time: 3 Hours**Max. Marks: 60**

(Answer all Five Units 5 x 12 = 60 Marks)

UNIT-I

- 1 a Write in detail about the Basic Operational Concepts with neat diagram. CO1 L1 6M
b Write in detail about Data Manipulation Instructions and types in it. CO1 L1 6M

OR

- 2 a Explain about the Structure of Bus and types of Bus with neat diagram. CO1 L1 6M
b Explain in detail about Instruction Cycle with neat sketch. CO1 L2 6M

UNIT-II

- 3 Draw the H/W Flowchart and H/W Algorithm for Multiplication for positive numbers with an suitable example. CO2 L3 12M

OR

- 4 a Discuss about decimal arithmetic unit. CO2 L2 6M
b Explain in detail about Floating point numbers, its operations and implementing it. CO2 L2 6M

UNIT-III

- 5 a Explain about Micro Programmed Control with Micro Program Example diagram. CO3 L2 8M
b Describe Register Representations and way it is used. CO3 L2 4M

OR

- 6 a Illustrate Logic Micro Operations with neat representations. CO3 L2 6M
b Describe about Bus transfer with neat diagram. CO3 L2 6M

UNIT-IV

- 7 a Explain about Magnetic Disks in detail with neat diagram. CO4 L2 6M
b List out some differences between RAM & ROM? CO4 L2 6M

OR

- 8 Consider the following Page reference string: CO4 L3 12M
7,0,1,2,0,3,0,4,2,3,0,3,2,1,2,0,1,7,0,1.
Explain the FIFO and LRU page replacement algorithms.

UNIT-V

- 9 a Define parallel processing? How one can achieve parallel processing with single CPU. CO5 L2 6M
b List out the conflicts in pipelining and explain about it. CO5 L1 6M

OR

- 10 a Explain about Inter Process Communication & Synchronization in detail. CO5 L2 8M
b Summarize various characteristics of Multiprocessor. CO5 L2 4M

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